

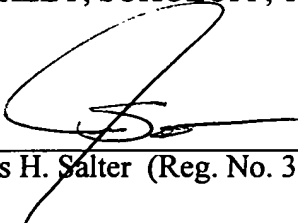
REMARKS

Applicants respectfully request consideration of the subject application as amended herein.

If there are any additional charges, please charge them to our Deposit Account No. 02-2666. If a telephone conference would facilitate the prosecution of this application, the Examiner is invited to contact James H. Salter at (408) 720-8300.

Respectfully submitted,
BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 6/17/, 2002


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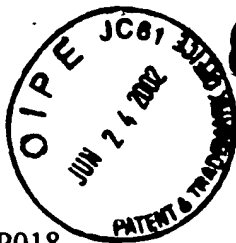
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[0003] The request stream from each different initiator can be described as a thread. If a DRAM scheduler does not re-order requests from the same thread, intra-thread request order is maintained, and the overall DRAM request order is simply an interleaving of the sequential per-thread request streams. This is the definition of Sequential Consistency, the strongest memory ordering model available for systems that include multiple initiator components. [](For further discussion regarding Sequential Consistency, see [L.] Leslie Lamport[], How to Make a [Multi-processing] Multiprocessor Computer That Correctly Executes Multiprocess Programs[], IEEE Transactions [o]n Computers, [C-28(9):241-248,] Vol. C-28, No. 9, September 1979[.]], pgs. 690-691.)

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